IDS - 04/12/2005 TED STATES PATENT AND TRADEMARK

JC13 Rec'd PCT/PTO

Appl. No.

TBD

Applicant

Shunsuke Hirano et al. Herewith

Filed Title

MODULATOR AND CORRECTION METHOD THEREOF

Int'l. Application No.

PCT/JP2004/000061

Int'l. Filing Date

January 8, 2004

Conf. No.

TBD TBD

TC/A.U. Examiner

TBD

Customer No.

000,116

Docket No.

38028

INFORMATION DISCLOSURE STATEMENT

Mail Stop PCT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir/Madam:

In accordance with 37 C.F.R. § 1.98, applicant is submitting herewith Form PTO-1449 listing references for consideration by the Examiner. A copy of the International Search Report is enclosed. Copies of the references cited in the International Search Report are not included because they were previously sent by the International Bureau.

Respectfully submitted,

PEARNE & GORDON LLP

By:

1801 East 9th Street **Suite 1200** Cleveland, Ohio 44114-3108 (216) 579-1700

Date: April 12, 2005

10/531050

Form PTO-1449

U.S. DE TMENT OF COMMERCE D TRADEMARK OFFICE

ATTY. DOCKET 38028

SERIAL NO.

TBD

INFORMATION DISCLOSURE CITATION BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)

Shunsuke Hirano et al.

GROUP ART UNIT: FILING DATE: Page 1 of 1 Herewith **TBD** U.S. PATENT DOCUMENTS Examiner Document No. Date Name Class Subclass Filing Date Initial -DM Α 6,008,703 12/1999 Perrott et al. В C D E F G Η 1 J K L FOREIGN PATENT DOCUMENTS Document No. Date Class Subclass Translation Country DM M 6-326518 11/1994 JP Cited in ISR DM 2001-156629 N 6/2001 JР Cited in ISR DM P 9-289447 11/1997 JP Cited in ISR DM Q 2002-207527 7/2002 JР Cited in ISR OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.) R S Т Date Considered 09/28/2006 /David Mis/ (09/28/2006) Examiner:

*Examiner:

Initial if reference considered, regardless of whether citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.

10/531,050

Applicant

Shunsuke Hirano et al.

Filed

April 12, 2005

Title

"MODULATOR AND CORRECTION METHOD

THEREOF"

Confirmation No.

5055

TC/A.U.

2817

Examiner

TBD

Customer No.

116

Docket No.

38028

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Va. 22313-1450

Sir/Madam:

In accordance with Rule 56, applicants are aware of the publications listed in the European Search Report (ESR, copy enclosed) of a corresponding European application and in the enclosed copy of Patent Office Form 1449. A copy of each of the publications is enclosed herewith.

The undersigned hereby certifies that each item of information contained in this Supplemental Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Supplemental Information Disclosure Statement.

> I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

> > Jeffrey J. Sopko

Name of Attorney for Applicant(s

April 11, 2006

Date

Application No.: 10/531,050

April 11, 2006 Page 2 of 2

If there are any fees required by this communication, please charge such fees to our Deposit Account No. 16-0820, Order No. 38028.

Respectfully submitted,

PEARNE & GORDON LLP

Βv

1801 East 9th Street Suite 1200 Cleveland, Ohio 44114-3108

(216) 579-1700

April 11, 2006

Form PTO-1449			J.S. DEPARTMEI ATENT AND TR	ATTY. DOCKET NO. 38028				SERIAL NO. 10/531,050		
SUPPLE	APPLICANT: Shunsuke Hirano et al.									
APR 17 2006 USA SEVERAL SHE			3 IF NECESSAR	FILING DATE: April 12, 2005				GROUP ART UNIT: 2817		
Paran	RABEN		U.S. PAT	ENT DOCUME	NTS	-			v	
Examiner Initial		Document No.	Date Name		С		lass Sub		lass	Filing Date If Appropriate
·	Α									
	В	·						_		
	С									
	D							<u> </u>	(
	E							·		
<u> </u>	F					_				
	G	:			•	+			i	
	Н			·			-			
	I				<u> </u>	+				
	K					+				
		F	OREIGN P	ATENT DOCU	MENTS	;				<u> </u>
		Document No.	Date	Country	Class		Sub	class	Tra	anslation
DM	L	2 809 890	12/2001	FRANCE					Cited on ESR- English Abstract	
	М									
	N			 						
	P								<u> </u>	- · · · · · · · · · · · · · · · · · · ·
	Q	<u>L</u>								
		OTHER REFERE		-						
DM	R	Perrott et al., "A 27-mW CMOS Fractional -N Synthesizer Using Digital Compensation for 2.5-Mb/s GFSK Modulation, 12/1997, pages 2048-2060 (Cited on ESR-English Text)								
,	s							<u>.</u> _		
·	Т									
Examiner	Examiner: /David Mis/ (09/28/2006) Date Considered 09/28/2006									
*Examine	т:			ss of whether citation is considered. Include cop						